

Refine Search

Search Results -

Term	Documents
REASSIGN	1663
REASSIGNS	696
(REASSIGN AND 49).USPT.	0
(L49 AND REASSIGN).USPT.	0

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L53

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Monday, April 11, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L53</u>	L49 and reassign	0	<u>L53</u>
<u>L52</u>	L49 and reassign adj channel	0	<u>L52</u>
<u>L51</u>	L49 and channel adj relocation	0	<u>L51</u>
<u>L50</u>	L49 and relocate adj channel	0	<u>L50</u>
<u>L49</u>	image adj processing and hard adj drive and channels and reserve	83	<u>L49</u>
<u>L48</u>	L47 and image adj processing	1	<u>L48</u>
<u>L47</u>	L38 and image	7	<u>L47</u>
<u>L46</u>	L38 and relocate adj channel	0	<u>L46</u>
<u>L45</u>	L38 and reassigning	0	<u>L45</u>
<u>L44</u>	L38 and reassigned	0	<u>L44</u>
<u>L43</u>	L38 and re-assigned	0	<u>L43</u>

<u>L42</u>	L38 and re-assigning	0	<u>L42</u>
<u>L41</u>	L38 and reassign	0	<u>L41</u>
<u>L40</u>	L38 and re-assigning adj channel	0	<u>L40</u>
<u>L39</u>	L38 and reassign adj channel	0	<u>L39</u>
<u>L38</u>	hard adj disk and reserve adj channels	18	<u>L38</u>
<u>L37</u>	hard adj disk adj storage and channels and reserve adj channel	0	<u>L37</u>
<u>L36</u>	L34 and disk	0	<u>L36</u>
<u>L35</u>	L34 and hard adj disk	0	<u>L35</u>
<u>L34</u>	reserve adj channels and reassign adj channels	3	<u>L34</u>
<u>L33</u>	L30 and reserving	0	<u>L33</u>
<u>L32</u>	L30 and reserved	1	<u>L32</u>
<u>L31</u>	L30 and reserve	0	<u>L31</u>
<u>L30</u>	channels adj reassigned and hard adj disk	3	<u>L30</u>
<u>L29</u>	channels adj reassigned and hard adj disk and storage and reserve	0	<u>L29</u>
<u>L28</u>	L22 and reassigning	0	<u>L28</u>
<u>L27</u>	L22 and reassigned	0	<u>L27</u>
<u>L26</u>	L2 and reassigning	0	<u>L26</u>
<u>L25</u>	L22 and re-assign	0	<u>L25</u>
<u>L24</u>	L22 and reassign	0	<u>L24</u>
<u>L23</u>	L22 and reserve	0	<u>L23</u>
<u>L22</u>	L21 and channels	3	<u>L22</u>
<u>L21</u>	read adj image adj data and hard adj disk adj storage	11	<u>L21</u>
<u>L20</u>	read adj image	11678	<u>L20</u>
<u>L19</u>	L18 and channels	1	<u>L19</u>
<u>L18</u>	L16 and image	1	<u>L18</u>
<u>L17</u>	L16 and hard	0	<u>L17</u>
<u>L16</u>	reassign adj channels and disk adj storage	1	<u>L16</u>
<u>L15</u>	L12 and reassign	0	<u>L15</u>
<u>L14</u>	L13 and reassign	0	<u>L14</u>
<u>L13</u>	L11 and reserve	3	<u>L13</u>
<u>L12</u>	L10 and reserve	2	<u>L12</u>
<u>L11</u>	L9 and plurality adj channels	8	<u>L11</u>
<u>L10</u>	L9 and multiple adj channels	3	<u>L10</u>
<u>L9</u>	L1 and hard adj disk	70	<u>L9</u>
<u>L8</u>	l1 and hard adj disk adj storage	0	<u>L8</u>
<u>L7</u>	L3 and multiple adj channels	0	<u>L7</u>
<u>L6</u>	L3 and plurality adj channels	3	<u>L6</u>
<u>L5</u>	L3 and unused adj channels	0	<u>L5</u>
<u>L4</u>	L3 and l1	0	<u>L4</u>
<u>L3</u>	controlling adj image adj processing	320	<u>L3</u>
<u>L2</u>	L1 and control adj image adj processing	0	<u>L2</u>